



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 10/050,347  
Filing Date ..... January 15, 2002  
Inventor ..... Gurtej S. Sandhu et al.  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2813  
Examiner ..... Laura M. Schillinger  
Attorney's Docket No. .... MI22-1897  
Title: Methods of Forming Transistors

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

References -- See Attached Form PTO-1449

The attached Form PTO-1449 is submitted in compliance with 37 CFR §1.56. Pursuant to FEDERAL REGISTER, Vol. 69, No. 182, pg. 56542 (September 21, 2004), no copies of any cited U.S. patents or U.S. published applications are included herewith. Copies of all other cited art are attached. No admission is made regarding whether the listed references are prior art.

Respectfully submitted,

Dated: December 5, 2006

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Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			ATTY. DOCKET NO. MI22-1897		SERIAL NO. 10/050,347	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)					APPLICANT Gurtej S. Sandhu et al.			
					FILING DATE January 15, 2002		GROUP 2813	
U.S. PATENT DOCUMENTS								
*Examiner Initial		Document Number	Date	Name	Class	Sub- class	Filing Date If Appropriate	
	AA	6,060,406	05/09/00	Alers, et al.				
	AB							
	AC							
	AD							
	AE							
	AF							
FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Country	Class	Sub- class	Translation	
							Yes	No
	AN							
	AO							
	AP							
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)								
	AS							
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